

STL9524

In the Claims:**Please amend claims 4, 5, 6, 8, and 13 as follows:**

- Sub C2
4. (once amended) The method of claim 1 further comprising applying a sequence of voltages during power-on, wherein a second voltage is activated a specific amount of time after a first voltage was activated.
5. (once amended) The method of claim 1 further comprising applying a sequence of voltages during power-off, wherein a second voltage is deactivated a specific amount of time after a first voltage was deactivated.
6. (once amended) The method of claim 1 further comprising adjusting a combination of variables selected from the group consisting of voltage disruption frequency, voltage disruption time duration and voltage disruption magnitude.
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8. (once amended) An electronic device power tester, comprising:
- (a) at least one power source;
 - (b) circuitry coupled to the at least one power source, the circuitry being configured to produce a voltage disturbance and repeat the voltage disturbance for a specified number of instances; and
 - (c) a connector linked from the circuitry to a device.